ASEOBJECT: hHmgr: 740506bb

Abusing GDI for ring0 exploit primitives:



Nicolas A. Economou Diego Juarez





AGENDA

- Review of Kernel Protections
- Arbitrary Write: Explanation
- Current ways of abusing kernel arbitrary writes
- Review PvScan0 technique
- Explain PvScan0 extended technique
- Intersection of the section of th
- Conclusions



PROTECTION MECHANISMS

- Integrity Levels: call restrictions for applications running in Low Integrity Level – since Windows 8.1
- KASLR: Address-space layout randomization (ASLR) is a well-known technique to make exploits harder by placing various objects at random, rather than fixed, memory addresses.
- SMEP: Supervisor Mode Execution Prevention allows pages to be protected from supervisor-mode instruction fetches. If enabled, software operating in supervisor mode cannot fetch instructions from linear addresses that are user mode reachable.

BUZEORAFCI:

Basers

WHAT IS AN ARBITRARY WRITE



ARBITRARY WRITE

- An arbitrary write is the result of exploiting a bug, it allows an attacker to place data under his control at an address of his choosing. (AKA Write-What-Where)
- Can be used to disrupt execution flow (write function pointer, vftable, etc), and sometimes even be turned into a read/write primitive (re-using internal data structures to your advantage).
- Examples: Heap overflows, many kinds of memory corruption and use-after-free bugs.

BASEOBJECT:

hHmgr: 740506bb ulShareCount: 00000000 cExclusiveLock: 0000 BaseFlags: 000

CURRENT TECHNIQUES





- Integrity levels appeared in Windows Vista
- Low Integrity Level in Windows 8.1 suppressed all the kernel addresses returned by NtQuerySystemInformation
- The most affected exploits are Local Privilege Escalation launched from sandboxes (like IE, Chrome, etc).

CORE SECURITY

CALL RESTRICTIONS

Running in Medium Integrity Level

 You know where the kernel base is, process tokens, some kernel structs, etc.

Exploitation tends to be "trivial"

Running in Low Integrity Level

- You can't rely on NtQuerySystemInformation
- You need a memory leak (**second vulnerability**) to get a predictable kernel address.
- Without memory leaks exploitation tends to be much harder.



LATESTS TECHNIQUES

use GDI objects:

Abusing GDI for ring0 exploit primitives

Diego Juarez

Windows Kernel Exploitation : This Time Font hunt you down in 4 bytes

KEEN TEAM

use Windows Paging Tables:

<u>Getting Physical: Extreme abuse of Intel based Paging Systems</u> Nicolas A. Economou - Enrique E. Nissim

use Windows HAL's HEAP:

 <u>Bypassing kernel ASLR – Target: Windows 10 (remote bypass)</u> Stéfan Le Berre - Heurs

BASEOBJECT:

hHmgr: 740506bb hlShareCount: 00000000 hExclusiveLock: 0000 BaseFlags: 000

Why GDI OBJECTS?



Why GDI objects ?

- Easy to understand/manipulate
- Kernel object addresses leaked to user-mode processes
- Common structure for all Windows versions
- Technique first discussed by <u>KEEN TEAM</u> (as far as we know <a>(2))

Baserso

WHAT CAN BE DONE

SECURITY

WHAT CAN BE DONE? ^A



BASEOBJECT:

hHmgr: 740506bb ulShareCount: 00000000 cExclusiveLock: 0000 BaseFlags: 000

Reviewing PvScan0 TECHNIQUE

CORE SECURITY

PvScan0 Technique





PvScan0 Technique

Abusing GDI for ring0 exploit primitives: RELOADED

By knowing a GDI handle, we can know the offset of its entry in the table.

addr = PEB.GdiSharedHandleTable + (handle & 0xffff) * sizeof(GDICELL64)

Say we call CreateBitmap and it returns HBITMAP = 0x0F050566.

| | kd> db 0x0000001e` | 1bf8 | 0000 |) + | 0×18 | * (|)566 | 5 | | | | | | | |
|-------------------------|--|------|------|--------------|---------------|-----|------|----------------|------------|----|----|----------|-----------|-----------|----------|
| | 0000001e`1 <mark>bf00190-</mark> | 00 | 10 a | a2 4 | 0 01 | f 9 | ff | ff-14 | ΟЪ | 00 | 00 | 05 | 0f | 05 | 40 |
| | 0000001e`1bf881a0 | 00 | 00 (|)0 0 | 0 00 | 00 | 00 | 00 - 10 | c0] | Ь7 | 40 | 01 | £9 | ff | ff |
| | 0000001e^1bf881b0 | | 00 (| | 0 05 | | 05 | 40 - 00 | 00 | | | | 0.0 | | 00 |
| typedef struct { OxFF | FF90140a210001pf881c0 | | | | | | | ff-48 | 04 | | | 05 | 2.2 | 05 | 40 |
| PVOID64 pKernelAddress; | 0000001 c`1 bf881d0 | | 00 (| | | | | 00 - 10 | 001 | Ь7 | 40 | | £9 | | ff |
| USHORT wProcessId; | 0000001e`1bf881e0 | 00 | 00 0 | <u>)0 0</u> | 0 08 | 0.9 | 08 | 40-00 | 00 | 00 | 00 | 00 | 00 | 00 | 0.0 |
| USHORT wCount; | 0000001e 16188110 0000001e 16188200 | | an i | 36 4 38 9 | | | | 11-48 00-00 | U4 40 | | 40 | Ua N1 | UБ f 9 | Ua. ff | 4U ff |
| USHORT wUpper; 🔫 | | | | | | | | | | | | | | | |
| USHORT wType; | | | | | | | | | | | | | | | |
| PVOID64 pUserAddress; | | | | | | | | | | | | | | | |
| } GDICELL64; | | | | | | | | | | | | | | | |
| | | | | | | | | | | | | | | | |



PvScan0 Technique





Although we cannot access **SURFACE**, **BASEOBJECT** or **SURFOBJ** members from user-mode code, nothing stops us from calculating their address.



This is interesting, because controlling this single pointer can give us memcpy() of any virtual address, and comes free with a very convenient way to invoke this functionality from **ring3**...**even at LOW INTEGRITY.**



PvScan0 Technique



- Use handles to lookup GDICELL, compute pvScan0 address
- Use vulnerability to write Worker's pvScan0 address as Manager's pvScan0 value.
- Use SetBitmapBits on Manager to select address.
- Use GetBitmapBits/SetBitmapBits on Worker to read/write previously set address.



C R PvScan0 Technique



C R PvScan0 Technique



C RE PvScan0 Technique





PvScan0 Technique

Abusing GDI for ring0 exploit primitives: RELOADED

GetBitmapBits(hWorker, len, readbuffer);

 hWorker = 0x20050555

GDI_TABLE_ENTRY:

 pKernelAddress:
 ffff90142352000

 wProcessId:
 00000b9c

 wCount:
 0000

 wUpper:
 2005

 wType:
 4005 (GD10bjType_SURF_TYPE)

 pUserAddress:
 0000000000000000

BASEOBJECT:

hHmgr: 20050555 ulShareCount: 00000000 cExclusiveLock: 0000 BaseFlags: 0000 Tid: 00000000000000000

SURFOBJ:

BASEOBJECT:

hHmgr: 740506bb ulShareCount: 00000000 cExclusiveLock: 0000 BaseFlags: 000

PvScan0 Extended TECHNIQUE





- It adds a new step to the original technique
- It consists of an overwrite of a **different** SURFOBJ property



PvScan0 Extended

- Keen Team touched on the subject at their presentation named above.
- We use/described this technique in the blogpost "MS16-039 – "Windows 10" 64 bits Integer Overflow exploitation by using GDI objects".



If we call **CreateBitmap**:

CreateBitmap (nWidth, nHeight, 1, cBitsPerPel, lpvBits);

An then we call **GetBitmapBits**/**SetBitmapBits SURFACE** bounds will be validated by:

size = nWidth * nHeight * cBitsPerPel;

It means we can't access beyond the object limits (as expected)



PvScan0 always* points only a few bytes ahead, the pixel data pointed to by PvScan0 is contiguous to the SURFOBJ header.



*doesn't HAVE to, but does



PvScan0 Extended

Abusing GDI for ring0 exploit primitives: RELOADED

The **SURFOBJ.sizlBitmap** property (x,y size)

typedef struct { ULONG64 dhsurf; // 0x00 ULONG64 hsurf; // 0x08 ULONG64 dhpdev; // 0x10 ULONG64 hdev; // 0x18 SIZEL siz1Bitmap; // 0x20 ULONG64 cjBits; // 0x28 ULONG64 pvBits; // 0x30 ULONG64 pvScan0; // 0x38 ULONG32 1Delta; // 0x40 ULONG32 iUnig; // 0x44 ULONG32 iBitmapFormat; // 0x48 USHORT iType; // 0x4C USHORT fjBitmap; // 0x4E } SURFOBJ64; // sizeof = 0x50

typedef struct tagSIZE {
 LONG cx;
 LONG cy;
} SIZE, *PSIZE;

CX

C++

Specifies the rectangle's width. The units depend on which function uses this.

cy

Specifies the rectangle's height. The units depend on which function uses this.



Abusing GDI for ring0 exploit primitives: RELOADED

The SURFOBJ.sizlBitmap property represents width and height of the SURFACE

If sizlBitmap.cx and/or sizlBitmap.cy are overwritten

 The SURFACE can be enlarged

It means we get read/write access beyond the bounds of the pixel data buffer!



The idea is to turn on some bits to enlarge the SURFACE.

 We can use any* arbitrary write, aligned or not, controllable or undefined:

> QWORD (8-byte) - DWORD (4-byte) WORD (2-byte) - BYTE (1-byte) single BIT!

× *NULL writes can't be used ☺







Abusing GDI for ring0 exploit primitives: RELOADED

MS16-039 (CVE-2016-0165) exploit after heap overflow

| 🛤 kernel debug (x86) | | | | | | |
|---------------------------|---|-----------|------------|----------------------|---|-----|
| kd> dq fffff901'70 | 576bf0+10 | | | | | |
| fffff901'70576c00 | 00000000'01051070 | 00000000 | '00000000 | | | |
| fffff901`70576c10 | 00000000,00000000 | 00000000 | 00000000 | | | |
| fffff901`70576c20 | 0000000000001051070 | 00000000 | 00000000 | | | |
| fffff901 70576c30 | 00000000,00000000 | 00000001 | 00000052 < | SURFOBJ64.sizlBitmap | | |
| ttttt901 70576c40 | 0000000 00000148 | 11111901 | 70576e58 | | | |
| | 11111901 /05/6658 | 00009b12 | 00000148 | | | |
| TTTTTYUL /05/6C60 | 00010000 00000000 | 00000000 | 00000000 | | | |
| TTTTTYUI /00/0C/0 | 00000000 04800200 | 000000000 | 000000000 | | | |
| Ruz g Reasknaint 1 hit | | | | | | |
| wip32kbase RGNMEMO | R l···ufreate+0v187· | | | | | |
| fffff960'bedae2f7 | 8hf0 mou | u esi | eav | | | 100 |
| kd> da fffff901'70 | 576bf0+10 | · 631 | , cun | | | |
| fffff901'70576c00 | 00000001 '00000000 | 00000000 | 'ffffffff | | | |
| fffff901'70576c10 | fffff901'70575fb0 | 00000000 | 00043333 | | | |
| fffff901'70576c20 | ffffffff 00000000 | 04333300 | 04333200 | | | |
| fffff901`70576c30 | 00000001'00000000 | 00000001 | ffffffff< | SURFOBJ64.sizlBitmap | | |
| fffff901'70576c40 | 00000000'00000148 | fffff901 | °70576e58 | | | |
| fffff901`70576c50 | fffff901`70576e58 | 00009b12 | 00000148 | | | |
| fffff901 70576c60 | 00010000`00000006 | 00000000 | 00000000 | | | |
| fffff901 70576c70 | 000000000000000000000000000000000000000 | 000000000 | .00000000 | | _ | |
| kd> | | | | | | |







PvScan0 Extended



- ✓ <u>IMPORTANT</u>: Any adjacent kernel structure could be effortlessly manipulated after enlarging a SURFACE
- Finally, it's interesting to say that:
 SURFOBJ.cjBits is set to nWidth * nHeight * cBitsPerPel
 However, this property is not used to validate SURFACE bounds after the header is corrupted!

BASEOBJECT:

hHmgr: 740506bb ulShareCount: 00000000 cExclusiveLock: 0000 BaseFlags: 000

MS16-039 LIVE DEMO

Target:

Windows 10 x64 v1511 Scenario:

Running in **Low Integrity Level** Objective:

> Get **SYSTEM** privileges by using PvScan0 Extended technique

BASEOBJECT:

hHmgr: 740506bb hlShareCount: 00000000 ExclusiveLock: 0000 BaseFlags: 000

Windows 10 v.1607 FIX







Windows Kernel 64-bit ASLR Improvements

Predictable kernel address space layout has made it easier to exploit certain types of kernel vulnerabilities

| | 47 | 39 38 | 30 29 | 21 20 | 12 11 | 0 |
|----------------|----------------------------|--------|---|-----------------------------------|-----------|-------------------------------------|
| Linear address | PML4 | Direct | tory ptr Dire | ctory | Table | Offset |
| 256 | Paged pool System cache | | System re randomiz ✓ Non-p ✓ Paged | gion PM ed baged pc pool | L4 entrie | es are Getting Physic |
| - | - | | System | n cache | Extr | eme abuse of Inte Paging Systems |
| | | | PFN d | atabase | _ | |
| | | | V Page t | ables | | Bypassing kern |
| | | | i uge i | abics | | Target : Winde |

Various address space disclosures have been fixed

- ✓ Page table self-map and PFN database are randomized
 - Dynamic value relocation fixups are used to preserve constant address references
- ✓ SIDT/SGDT kernel address disclosure is prevented when Hyper-V is enabled
 - Hypervisor traps these instructions and hides the true descriptor base from CPL>0
- ✓ GDI shared handle table no longer discloses kernel addresses

| Tactic | Applies to | First shipped |
|----------------------------------|--------------------------|---|
| Breaking exploitation techniques | Windows 10 64-bit kernel | August, 2016 (Windows 10 Anniversary Edition) |



Windows 10 v1607 Fix

ows Kernel 64-bit ASLR Improvements

ddress space layout has made it easier to exploit certain types of kernel vulnerabilities

ddress space layout is now dynamic

 47
 39
 38
 30
 29
 21
 20
 12
 11
 0

 sss
 PML4
 Directory ptr
 Directory
 Table
 Offset

ystem region PML4 entries are andomized

Non-paged poo

- 🖌 Paged po
- V System c
- 🗸 PFN databa
- 💉 Page table
- 🚽 🖌 ... and so or

Various address space disclosures have been fixed

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- SIDT/SGDT kernel address disclosure is prevented when Hyper-V is enabled
 - Hypervisor traps these instructions and hides the true descriptor base from CPL>0

✓ GDI shared handle table no longer discloses kernel addresses

First shipped

August, 2016 (Windows 10 Anniversary Edition





Windows 10 v1607 Fix





Windows 10 v1607 Fix





BASE OBJECT:

Hmgr: 740506bb ulShareCount: 00000000 cExclusiveLock: 0000 BaseFl<u>ags: 000</u>

Lets find a new one! 🤓

1



BASEOBJECT:

hHmgr: 740506bb ulShareCount: 00000000 cExclusiveLock: 0000 BaseFlags: 000

BYPASSING Windows 10 v. 1607 KASLR

C RE BYPASSING v1607 KASLR





BYPASSING v1607 KASLR

Objects indexed by this table: User Objects
 <u>https://msdn.microsoft.com/en-us/library/windows/desktop/ms724515(v=vs.85).aspx</u>

| User object | Overview |
|-------------------|--|
| Accelerator table | Keyboard Accelerators |
| Caret | Carets |
| Cursor | Cursors |
| DDE conversation | Dynamic Data Exchange Management Library |
| Hook | Hooks |
| Icon | Icons |
| Menu | Menus |
| Window | Windows |
| Window position | Windows |

EXAMPLE SECURITY BYPASSING v1607 KASLR

User Objects < 4KBUser Objects => 4KB (Large Pool) POOL TYPE 0x21 (PagedPoolSession) NonPagedPool POOL TYPE 0x29 (PagedPoolSession+0x8(?!)) But we need **GDI objects**, so what's the point **?** GDI objets use the **SAME** heap as User Objects! GDI Objects < 4KB• 0 POOL TYPE 0x21 (PagedPoolSession) GDI Objects => 4KB (Large Pool) -**NonPagedPool**



- Knowing the previous, it's possible to predict GDI ALLOCATIONS in KERNEL SPACE
- So, if we:
 - ✓ Alloc a USER object (we know its KADDRESS)
 - ✓ Free the same USER object
 - ✓ Alloc a GDI object with size equal to USER object
- We have a high probability to infer where the GDI object was allocated (Free List mechanism!)







BYPASSING v1607 KASLR

KMALLOC/KFREE primitives

• For objects < 4KB

KALLOC: win32u!NtUserConvertMemHandle() KFREE: win32u!NtUserSetClipboardData() + EmptyClipboard()

• For objects >= 4KB

KALLOC: user32!CreateAcceleratorTableA()
KFREE: user32!DestroyAcceleratorTable()

EXAMPLE SECURITY BYPASSING v1607 KASLR

Try to use >= 4KB objects if possible.

HACCEL WINAPI CreateAcceleratorTable(
 In LPACCEL lpaccl,
 In int cEntries
);

lpaccl [in]

Type: LPACCEL

An array of ACCEL structures that describes the accelerator table.

cEntries [in]

Type: int

The number of ACCEL structures in the array.

This must be within the range 1 to 32767 or the function will fail.

KMALLOC/KFREE primitives

For objects >= 4KB KALLOC: user3

KFREE:

user32!CreateAcceleratorTableA() user32!DestroyAcceleratorTable()

 Allocations >= 4KB are aligned to 0xXXXXXXXXXXXXX000 (12 bits)

 The granularity is 4KB (E.g 5KB request returns 8KB buffer)

 If allocations are big, it's less likely that a freed chunk will randomly be reused by the Windows kernel during exploitation









BASEOBJECT:

hHmgr: 740506bb hlShareCount: 00000000 ExclusiveLock: 0000 aseFlags: 0000

KMALLOC/KFREE/KMALLOC LIVE DEMO

Target:

Windows 10 x64 v1511 Scenario:

Running in **Low Integrity Level** Objective:

- Show kernel allocations

BASEOBJECT:

nHmgr: 740506bb nlShareCount: 00000000 ExclusiveLock: 0000 aseFlags: 0000

FINAL LIVE DEMO

Target:

Windows 10 x64 v1607 Scenario:

Running in **Low Integrity Level** Objective:

- Simulate a kernel arb.write
- Bypass KASLR using GDI objects
- Get "system" privileges

BASEOBJECT:

hHmgr: 740506bb hlShareCount: 00000000 ExclusiveLock: 0000 BaseFlags: 000

CONCLUSIONS



CONCLUSIONS

- KASLR can be still bypassed in all Windows versions.
- User objects table (gSharedInfo->aheList) shouldn't leak a real kernel pointer.
- **GDI objects addresses** can be inferred via user objects.
- **SURFOBJ.cjBits** should be used to validate the BITMAP size.

Baseraugs

QUESTIONS

BASEOBJECT:

Hmgr: 740506bb ilShareCount: 000000000 ExclusiveLock: 0000 BaseFlags: 00

THANK YOU



DEE